

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE
(UGC-AUTONOMOUS)

M.Tech II Year II Semester - Project Viva Voce & Seminar Examination - Nov 2016

RESULTS


VLSI & Embedded Systems


S.No	Roll No	Project Work - (14VES22D01)		Seminar - (14VES22S01)		Month & Year of Passing
		L.G	C	L.G	C	
1	14691D6801	A+	16	A+	2	Nov 2016
2	14691D6811	A+	16	A+	2	Nov 2016
3	14691D6812	Ab	0	A+	2	Nov 2016
4	14691D6818	B+	16	A+	2	Nov 2016
5	14691D6820	A	16	O	2	Nov 2016

Machine Design

S.No	Roll No	Project Work - (14MD22D01)		Seminar - (14MD22S01)		Month & Year of Passing
		L.G	C	L.G	C	
1	14695D1501	A+	16	O	2	Nov 2016
2	14695D1502	A+	16	O	2	Nov 2016
3	14695D1503	A	16	O	2	Nov 2016
4	14695D1504	A+	16	O	2	Nov 2016
5	14695D1505	A+	16	O	2	Nov 2016
6	14695D1506	A+	16	O	2	Nov 2016
7	14695D1507	A	16	O	2	Nov 2016
8	14695D1509	A	16	O	2	Nov 2016
9	14695D1510	A+	16	O	2	Nov 2016

Date: 23.11.2016


Controller of Examinations


PRINCIPAL
etc